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J.S. UTILITY Patent Applic	ation	
N'd O.LR.E.	PATENT DATE	
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER & mee L.
09/505949		712	222	2783	

Michael Chow Elango Ganesan John Phillips Nazar Zaidi

Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support

PATENT NUMBER

ISSUING C	LASSIFICATION		
CROSS REFERENCE(S)			
CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
	Continued on Issue Slip Inside File Jacket		

TERMINAL	DRAWINGS			CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G	
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subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)			
The term of this patent shall	,					
not extend beyond the expiration date of U.S Patent. No				ISSUE FEE		
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this patent have been disclaimed.	(Legal Instrum	ents Examiner)	(Date)			
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orm PTO-436A Rev. 6/99)		-	FILED WITH:		FICHE CD-RO	